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Application Number	10702152
Filing Date	2003-11-05
First Named Inventor	WANG
Art Unit	2419
Examiner Name	PEZZLO
Attorney Docket Number	08-1293

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	1	M. Karol, M. Hluchyj and S. Morgan in "Input versus Output Queuing on a Space-Division Packet Switch," IEEE Transactions on Communications, Vol COM-35, NO. 12, December 1987	<input type="checkbox"/>
	2	N. McKeown, S. Iyer and R. Zhang in "Routers with a Single Stage of Buffering," Proc. Of ACM SIGCOMM., August 2002	<input type="checkbox"/>
	3	N. McKeown in "SLIP: A scheduling Algorithm for INput-Queued Switches," IEEE Transactions on Networking, Vol. 7, No. 2, April 1999	<input type="checkbox"/>
	4	Y. Tamie and S.C. Chi, "The Symmetric Crossbar Arbiters for VLSI Communication Switches," IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 1, pp. 13-27 (1993)	<input type="checkbox"/>
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